<u>Claims</u>

We claim:

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- 1 1. A method of forming a heat exchanger, comprising:
- a. forming a manifold layer defining a plurality of apertures; and
 - b. forming an interface layer comprising one or more narrowing trenches, each aperture positioned on one side of a narrowing trench, whereby a path is defined from a first aperture, through a narrowing trench, and to a second aperture.
- 1 2. The method of claim 1, wherein the interface layer comprises a material exhibiting 2 anisotropic etching.
- 1 3. The method of claim 2, wherein the material comprises a <110> oriented silicon substrate.
- The method of claim 3, wherein forming an interface layer comprises etching the <110>
 oriented silicon substrate in an etchant to produce a <111> oriented surface defining a
 sloping wall of a narrowing trench.
- 1 5. The method of claim 4, wherein the etchant comprises potassium hydroxide (KOH).
- 1 6. The method of claim 4, wherein the etchant comprises tetramethyl ammonium hydroxide 2 (TMAH).

1 7. The method of claim 1, wherein the one or more narrowing trenches are formed by a 2 machining process selected from the group consisting of milling, sawing, drilling, 3 stamping, EDM, wire EDM, coining, die casting, and investment casting. 8. 1 The method of claim 1, wherein the one or more narrowing trenches are formed by a 2 process selected from the group consisting of electroplating, metal injection molding, 3 LIGA processes, and casting. 1 9. The method of claim 1, wherein the manifold layer and the interface layer are formed of a 2 monolithic device. 1 10. The method of claim 1, further comprising coupling the manifold layer to the interface 2 layer. 1 11. The method of claim 10, wherein coupling the manifold layer to the interface layer 2 comprises adhesively bonding the manifold layer to the interface layer. 1 12. The method of claim 10, wherein coupling the manifold layer to the interface layer 2 comprises thermally fusing the manifold layer to the interface layer. 1 13. The method of claim 10, wherein coupling the manifold layer to the interface layer 2 comprises anodically bonding the manifold layer to the interface layer. 1 14. The method of claim 10, wherein coupling the manifold layer to the interface layer 2 comprises eutectically bonding the manifold layer to the interface layer.

- 1 15. The method of claim 1, wherein the manifold layer comprises a material selected from the group consisting essentially of a plastic, a glass, a metal, and a semiconductor.
- 1 16. The method of claim 1, wherein forming the manifold layer comprises forming a first
 2 plurality of interconnected hollow fingers and a second plurality of interconnected hollow
 3 fingers, the first plurality of interconnected hollow fingers providing flow paths to the one
 4 or more first apertures and the second plurality of interconnected hollow fingers
 5 providing flow paths from the one or more second apertures.
- 17. The method of claim 16, wherein the first plurality of interconnected hollow fingers and the second plurality of interconnected hollow fingers lie substantially in a single plane.
- 1 18. The method of claim 16, further comprising coupling a pump to the first plurality of interconnected hollow fingers.
- 1 19. The method of claim 1, further comprising coupling a heat-generating source to the interface layer.
- The method of claim 19, wherein a bottom surface of the interface layer is integrally formed with the heat-generating source.
- The method of claim 19, wherein the heat-generating source comprises a semiconductor microprocessor.

- The method of claim 18, further comprising introducing a cooling material to the pump, so that the pump circulates the cooling material along the first plurality of interconnected hollow fingers, to the one or more first apertures, along a plurality of narrowing trenches, to the one or more second apertures, and to the second plurality of interconnected hollow fingers, thereby cooling the heat-generating source.
- 1 23. The method of claim 22, wherein the cooling material comprises a liquid.
- 1 24. The method of claim 23, wherein the liquid comprises water.
- 1 25. The method of claim 22, wherein the cooling material comprises a liquid/vapor mixture.
- The method of claim 1, wherein each aperture lies substantially in a single plane, parallel to a lower surface of the interface layer.
- The method of claim 1, wherein the manifold layer comprises a surface that extends into each narrowing trench and substantially conforms to a contour of each narrowing trench.
- The method of claim 1, wherein a narrowing trench has a depth:width aspect ratio of at least approximately 10:1.
- The method of claim 1, further comprising coupling an intermediate layer between the manifold layer and the interface layer, the intermediate layer comprising a plurality of openings positioned over the plurality of apertures, thereby controlling the flow of a cooling material to the paths.

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1	30.	A heat exchanger comprising:
2		a. a manifold layer defining a plurality of apertures; and
3		b. an interface layer comprising a plurality of narrowing trenches, each aperture
4		positioned on one side of a narrowing trench, whereby a path is defined from a
5	j	first aperture, through a narrowing trench, and to a second aperture.
1	31.	The heat exchanger of claim 30, wherein the interface layer comprises a material
2		exhibiting anisotropic etching.
1	32.	The heat exchanger of claim 31, wherein the material exhibiting anisotropic etching
2		comprises a <110> oriented silicon substrate.
1	33.	The heat exchanger of claim 32, wherein the interface layer is formed by etching the
2		<110> oriented silicon substrate in an etchant to produce a <111> oriented surface
3		defining a sloping wall of a narrowing trench.
1	34.	The heat exchanger of claim 33, wherein the etchant comprises potassium hydroxide
2		(KOH).
1	35.	The heat exchanger of claim 33, wherein the etchant comprises tetramethyl ammonium
2		hydroxide (TMAH).
1	36.	The heat exchanger of claim 30, wherein the narrowing trenches are formed by a
2		machining process selected from the group consisting of milling, sawing, drilling,
3		stamping, EDM, wire EDM, coining, die casting, and investment casting.

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The heat exchanger of claim 30, wherein the narrowing trenches are formed by a process 1 37. 2 selected from the group consisting of electroplating, metal injection molding, LIGA 3 processes, and casting. 38. The heat exchanger of claim 30, wherein the manifold layer and the interface layer are 1 2 formed of a monolithic device. 39. The heat exchanger of claim 30, wherein the manifold layer is coupled to the interface 1 2 layer. 40. The heat exchanger of claim 39, wherein the manifold layer is coupled to the interface 1 2 layer by adhesive bonding. The heat exchanger of claim 39, wherein the manifold layer is coupled to the interface 1 41. 2 layer by thermal fusing. 1 42. The heat exchanger of claim 39, wherein the manifold layer is coupled to the interface 2 layer by anodic bonding. The heat exchanger of claim 39, wherein the manifold later is coupled to the interface 1 43. 2 layer by eutectic bonding. 1 44. The heat exchanger of claim 30, wherein the manifold layer comprises a material selected

from the group consisting essentially of a plastic, a glass, a metal, and a semiconductor.

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1 45. The heat exchanger of claim 30, wherein the manifold layer comprises a first plurality of 2 interconnected hollow fingers and a second plurality of interconnected hollow fingers, the 3 first plurality of interconnected hollow fingers providing flow paths to the one or more 4 first apertures and the second plurality of interconnected hollow fingers providing flow 5 paths from the one or more second apertures. 1 46. The heat exchanger of claim 45, wherein the first plurality of interconnected hollow 2 fingers and the second plurality of interconnected hollow fingers lie substantially in a 3 single plane. 1 47. The heat exchanger of claim 45, further comprising a pump coupled to the first plurality 2 of interconnected hollow fingers. 1 48. The heat exchanger of claim 30, further comprising a heat-generating source coupled to 2 the interface layer. 1 49. The heat exchanger of claim 48, wherein the heat-generating source comprises a 2 semiconductor microprocessor. 1 50. The heat exchanger of claim 48, wherein the heat-generating source is integrally formed 2 to a bottom surface of the interface layer. 1 51. The heat exchanger of claim 30, wherein each aperture lies substantially in a single plane,

parallel to a lower surface of the interface layer.

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- The heat exchanger of claim 30, wherein the manifold layer comprises a surface that extends into each trench and substantially conforms to a contour of each narrowing trench.
- The heat exchanger of claim 30, wherein a depth:width aspect ratio for at least one of the plurality of narrowing trenches is at least 10:1.
- The heat exchanger of claim 30, further comprising an intermediate layer positioned between the manifold layer and the interface layer, the intermediate layer comprising a plurality of openings positioned over the plurality of apertures, thereby controlling the flow of a cooling material to the paths.